



Swami Keshvanand Institute of Technology, Management & Gramothan, Jaipur

(An Autonomous Institute Affiliated to Rajasthan Technical University, Kota)

(Accredited by NAAC with "A++" Grade)

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ELECTRO NEXUS

Empowering Tomorrow Through Electronics

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BIANNUAL
NEWSLETTER

Department of Electronics and Communication Engineering



From HoD Desk

Welcome Message from Leadership

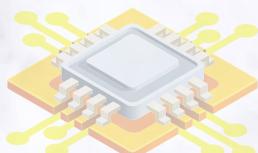
I would like to welcome you all to our Department of Electronics and Communication Engineering, a discipline that continues to revolutionize our technological landscape. ECE stands at the crossroads of innovation, offering a diverse array of courses and practical learning opportunities in Embedded Systems, IoT, VLSI Design, Advanced Communication Systems, and Signal Processing. Our comprehensive curriculum is meticulously crafted to align with global industry demands, positioning our graduates at the forefront of technological revolutions.

Our department prides itself on its state-of-the-art laboratories, experienced faculty, and a culture of academic excellence. We are committed to fostering entrepreneurial spirit, research, and industry collaboration, ensuring that every student develops the skills needed to tackle real-world challenges with creativity and confidence. Students are encouraged to participate in hands-on projects, internships, and innovation-driven activities, making them highly sought after by prominent industries and research organizations.



Prof. (Dr.) Praveen Kumar Jain

As we look to the future, the opportunities for ECE graduates are boundless—spanning sectors such as healthcare, semiconductor technology, telecommunications, space exploration, and renewable energy. By joining our vibrant and dynamic community, students not only gain technical prowess but also contribute meaningfully to society, creating technologies that transform lives. With a legacy of excellence and a vision for an impactful future, we invite you to be part of our journey. Together, let us inspire, innovate, and shape a smarter tomorrow.



From Editor Desk



Ms. Priyanka Sharma

Dear Readers,

It gives me immense pleasure to present the biannual edition of the ECE Department Newsletter of Swami Keshvanand Institute of Technology, Management & Gramothan (SKIT). This newsletter serves as a vibrant platform to showcase the creativity, technical innovations, and achievements of our students and faculty members. In every issue, we strive to highlight the spirit of inquiry, innovation, and excellence that defines the Electronics and Communication Engineering fraternity.

The field of ECE continues to evolve rapidly—bridging hardware with intelligent systems, IoT, embedded design, AI, and quantum technologies. Through this publication, we aim to inspire our readers to stay curious, explore new technologies, and contribute meaningfully to the ever-changing landscape of engineering.

I extend my heartfelt appreciation to all contributors for their dedication and teamwork in bringing this edition to life. I also encourage students to actively participate, share their innovative ideas, and continue pushing the boundaries of learning and research. Let this newsletter be a reflection of our collective growth, collaboration, and commitment to excellence.



VISION

To evolve the department as a center of excellence in the field of Electronics & Communication Engineering for enriched education, higher learning, research and development

MISSION

- M1: To Impart quality education in Electronics and Communication Engineering for better employability.
- M2: To Prepare students for being competent in dealing with industrial challenges.
- M3: To Equip students for lifelong learning and for serving the society.

PROGRAM EDUCATIONAL OBJECTIVES

To pursue their career successfully in the field of Electronics & Communication Engineering and advance in their profession.

1

To excel in pursuing higher education and lifelong learning.

2

To hold high ethical standards and work effectively in multidisciplinary teams with strong management and team work skills

3

OUTREACH ACTIVITIES

MOU SIGNED WITH JYOTI ELECTRONICS, AHMEDABAD

September 16, 2025

To foster industry-academia collaboration, the Center of Excellence in Antenna, Microwave, and RF Engineering at SKIT Jaipur has entered into a Memorandum of Understanding (MoU) with Jyoti Electronics, Ahmedabad. Established in 1981, Jyoti Electronics is a leading organization in the Indian RF and Microwave sector. The company is well known for providing advanced industry-standard software platforms, including circuit, system, and electromagnetic (EM) simulation tools.

It also specializes in antennas, cable assemblies, connectors, and testing & measurement solutions.

Key Objectives of the MoU

- Provide internship opportunities for B.Tech and M.Tech students.
- Organize Faculty Development Programs (FDPs), workshops, and technical training sessions.
- Strengthen student placement opportunities in the RF and Microwave domain.
- Offer hands-on exposure to advanced technical equipment and design software, enhancing student expertise in RF/Microwave design, antenna simulation, and testing.

The MoU was formalized during the S.A.C. Technology Day organized by Jyoti Electronics in Ahmedabad, where several eminent representatives and experts from SAC-ISRO participated and deliberated on emerging innovations in space research and RF technologies.

Representing SKIT Jaipur, Dr. Suman Sharma, Associate Professor, Department of ECE, attended the event. On behalf of Jyoti Electronics, Mr. Rajendra Agarwal (Managing Director) and Mr. Parth Agarwal (Technical Director) extended their best wishes to SKIT students. Expressing their delight over this collaboration, Mr. Jaipal Meel (Director, SKIT), Dr. Ramesh Kumar Pachar (Principal), Dr. R.K. Jain (Dean), and Dr. Praveen Kumar Jain (Head, ECE Department) congratulated the faculty and students, and highlighted the immense scope this partnership brings in advancing academic excellence and industry readiness in the RF domain.

एसकेआईटी और ज्योति इलेक्ट्रॉनिक्स के बीच हुआ एमओयू

आम-पास ज्ञान

जाव्हाहरुल इकबाली के शास्त्रात्मक इंस्टीट्यूट और ज्योति इलेक्ट्रॉनिक्स, वारपाल के सेटर ऑफ एंटेन्नासेंस इन एंटर्नेशनल मार्केटिंग एंड आरएन्सीनिंग में अहमदाबाद विश्व ज्योति इलेक्ट्रॉनिक्स के साथ एक ऑफ चुनिया। एसकेआईटी को ज्योति में विदेशी वायपल मॉल वे प्लॉटों पर, द्वारपाल इंटरॉफर वायपल को नहीं दिखा रहा। एसकेआईटी विश्व ज्योति आदानपेन वायपल में अहमदाबाद में आयोजित एस.ए.सी. टेलीकॉम्लेटरी के कार्यक्रम के दौरान हुआ। इस अवसर पर एसकेआईटी के कई विदेशी भौतिक और ज्योति में जुड़ने वाले अंतर्राष्ट्रीय तकनीकीर्ति नवाचारों पर चर्चा की गई। सुनन शर्मा, एसकेआईटी प्रोफेसर, एसकेआईटी ने



इस कार्यक्रम में भाग लिया। समाजीते का ज्योति की हाथ और एस.ए.सी. का

इंस्टीट्यूट के अवसर उल्लंघन कर्त्ता, ज्योति के लिए समर्पित सम्भावनाएं, ज्योति की मजबूती बनाना तथा एक निवास इन्स्टीट्यूट और टीकेटेक जैसे बायों में अधिकार वाकातरी और सापेलीय का प्रत्यक्ष अनुभव उत्पन्न करना।

कार्यक्रम में ज्योति इलेक्ट्रॉनिक्स के विशेषज्ञ इंस्टीट्यूट राजें आरेश और टीकेटेक ड्रायवर कर्मचारी जैसे आयोजन में प्रकाशित आईटी विद्युतियों की मुख्यतावानी दी। इस अवसर पर एसकेआईटी में वायपल मॉल (निवास) का सेंटर कूर्स यात्रा (शायरी), डॉ. आर.के. जैन (जीव) पर्सन का प्रोफेसर क. मार्टिन (विद्युतशाखा, इंसीट) विद्युतियों को बाजारी दी और कर्मकारों द्वारा जारी की छोटी जैसी वातावरिणी विनियोगों के लिए तैयार करेंगे।



OUTREACH ACTIVITIES

INDUSTRIAL VISIT TO JAIPUR METRO RAIL CORPORATION (JMRC)

August 23, 2025

The Department of Electronics and Communication Engineering, Swami Keshvanand Institute of Technology, Management & Gramothan (SKIT), Jaipur, organized an industrial visit to Jaipur Metro Rail Corporation (JMRC) for the students of ECE department. The visit aimed to provide students with valuable exposure to research methodologies, manpower planning, and human resource development practices adopted by JMRC.

Upon arrival, students completed the registration process and were warmly welcomed by the JMRC officials. The session began with a brief presentation introducing JMRC's vision, organizational functions, and major research domains. Students were acquainted with key personnel from JMRC who guided them throughout the visit, explaining the center's operational structure and research activities.

During the facility tour, students visited key departments such as research laboratories, data centers, and training units, where they gained insights into JMRC's ongoing projects related to manpower research, policy planning, and human resource development. The demonstration of technological tools and data-driven approaches used in manpower analytics was of particular interest to the students. An interactive Q&A session followed, allowing participants to engage directly with JMRC experts and researchers. The discussions helped to bridge the gap between academic knowledge and real-world manpower management practices.

Dr. Praveen Kumar Jain, Dr. Neeraj Jain, Ms. Harshal Nigam and Ms. Gloria Joseph coordinated this visit. The live demonstrations and facility interactions were highly valued, making the experience both informative and inspiring for all participants.



OUTREACH ACTIVITIES

INDUSTRIAL VISIT TO TECHNOS INSTRUMENTS, JAIPUR

October 6, 2025

The industrial visit was organized by the Department of Electronics and Communication Engineering, Swami Keshvanand Institute of Technology, Management & Gramothan (SKIT), Jaipur, for the third semester students as a part of their practical learning experience. The visit was coordinated by Dr. Neeraj Jain, Dr. Shubhi Jain, and Abhinandan Jain.

Students visited Technos Instruments, Sitapura, Jaipur, where they were warmly received and briefed about the company and its working environment. The main objective of the visit was to understand how proper energy supply can be maintained to meet the ever increasing demand for energy in industries. During the visit, students closely observed and learned about various advanced technologies such as 3D printing, drone technology, and electric vehicle technology.

They were shown how these technologies are installed, operated, and utilized for industrial purposes. The professionals at Technos Instruments guided the students through the processes and explained their practical applications in real-life industrial scenarios. The session also included demonstrations and discussions that helped the students connect their theoretical knowledge with actual industrial practices. Overall, the visit provided meaningful exposure to the latest technological developments and enhanced the students' understanding of modern engineering innovations.



OUTREACH ACTIVITIES

INDUSTRIAL VISIT TO BSNL, JAIPUR

October 6, 2025

The students of the Department of Electronics and Communication Engineering, Swami Keshvanand Institute of Technology, Management & Gramothan (SKIT), Jaipur, from the fifth semester, visited Bharat Sanchar Nigam Limited (BSNL), M.I. Road, Jaipur, for an industrial visit.

The objective of the visit was to provide students with practical exposure to telecommunication systems and network operations. During the visit, students gained valuable insights into GSM (Global System for Mobile Communication), network operating systems, broadband networks, and the integrated Network Operation Center (NOC).

Experts at BSNL explained the functioning of MSC (Mobile Switching Center) and GMSC (Gateway Mobile Switching Center) used in 2G, 3G, and 4G networks in detail.

In addition, students were given a live demonstration of C-PAN (Centralized Processing and Analysis Network) and data transfer system processes, enhancing their understanding of real-world communication infrastructure. The industrial visit was coordinated by Dr. Shubhi Jain, Dr. Pallav Rawal, Dr. Neeraj Jain, and Mr. Harshal Nigam, who ensured smooth organization and valuable learning for the students.

The visit proved to be an enriching experience, bridging the gap between theoretical learning and practical implementation in modern telecommunication systems.



OUTREACH ACTIVITIES

ADVANCED IOT TRAINING PROGRAM AT CSIR-CEERI, JAIPUR

October 6 - November 7, 2025

The Department of Electronics and Communication Engineering, Swami Keshvanand Institute of Technology, Management & Gramothan (SKIT), Jaipur, proudly announces the successful participation of its students in the Advanced IoT Training Program organized by the CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI), Jaipur.

Under the CSIR Integrated Skill Initiative – Phase III, sponsored by RIICO, Rajasthan, the one-month program, conducted from 6th October to 7th November, 2025, aimed to impart advanced technical skills in the field of the Industrial Internet of Things (IoT). Out of 150 applicants from reputed institutes across India, only 30 candidates were shortlisted, including eight students from SKIT's ECE Department, reflecting the institute's consistent academic excellence. The training curriculum was thoughtfully designed to provide participants with both theoretical insights and practical expertise. It covered Embedded Systems, Sensor Integration, PCB Design and Fabrication, Assembly and Testing, Software Development, and Robotics-based Automation.

Through hands-on sessions, expert lectures, and live demonstrations, students gained real-world exposure to IoT-enabled industrial systems and smart automation.

He highlighted CSIR-CEERI's initiatives for promoting skill-based learning and empowering young women engineers under national missions like Skill India and Viksit Bharat @2047.

Mr. Jaipal Meel, Dr. Ramesh Kumar Pachar, Dr. R. K. Jain, and Dr. Praveen Kumar Jain congratulated the students on the successful completion of the program.

A group photograph of a class of students and their teacher in a classroom setting. The students are seated in rows, and the teacher is standing in front of them. A display board is visible in the background, featuring text and diagrams related to the topic of the class.

The program was coordinated by Dr. Vijay Chatterjee, Principal Scientist at CSIR-CEERI, Jaipur who mentored participants in developing innovative IoT-based solutions. The valedictory ceremony, held on 31st October 2025, featured Dr. Meghendra Sharma, Secretary, Vigyan Bharati Rajasthan, as the Chief Guest.

एसकेआईटी के 8 छात्रों ने CSIR-CEERI में उन्नत औद्योगिक इंटरनेट ऑफ थिंग्स पर एक महीने का कौशल विकास कार्यक्रम किया पूरा



The institutional coordination was led by Dr. Monika Mathur, Department of ECE, SKIT. This achievement underscores SKIT's ongoing commitment to fostering innovation, technical excellence, and academic-industry collaboration.

The event concluded with certificate distribution and heartfelt appreciation from the participants for the expert mentorship, state-of-the-art facilities, and enriching learning experience.

OUTREACH ACTIVITIES



SHILP (SEMICONDUCTOR HIGH-IMPACT LEARNING PROGRAM) AT CSIR-CEERI, PILANI

December 15 -26, 2025

Under the aegis of Signed MOU with CEERI, a training program “SHILP (Semiconductor High-Impact Learning Programme)” was conducted at CSIR-CEERI, Pilani from 15th December to 26th December, 2025. This training was attended by the 5th semester students, a group of 13 students successfully participated in person in this program. The programme was designed to provide hands-on exposure and industry-oriented knowledge in the field of semiconductor technology.

During the training, participants gained practical experience in various semiconductor fabrication and characterization processes, including wafer cleaning, photolithography, dry and wet etching, metallization processes, thermal oxidation, chemical vapor deposition (CVD), rapid thermal annealing, metal lift-off, and device characterization. These sessions were conducted in well-equipped laboratories under the guidance of experienced scientists and technical staff.



The programme played a crucial role in bridging the gap between theoretical concepts and real-world industrial practices. It offered valuable insights into cleanroom protocols, fabrication workflows, and the challenges involved in semiconductor manufacturing, which are highly relevant to the current and future demands of the semiconductor industry. Overall, the training was extremely beneficial and significantly enhanced the participants' understanding of semiconductor processes from an industrial perspective.



CSIR-CEERI
Skill Development Programme

SHILP
Semiconductor High-Impact Learning Programme

Aims to Create Semiconductor Engineers
CSIR-CEERI welcomes graduate students to enter into the world of semiconductors for a self-reliant India

Practical sessions on

Wafer Cleaning	Metallization Process
Dry & Wet Etching	Chemical Vapor Deposition
Photolithography	Rapid Thermal Annealing
Metal Lift-off	Thermal Oxidation
	Device Characterization

BE FUTURE-READY FOR SEMICONDUCTOR POWERED INDIA

The exposure received at CSIR-CEERI has strengthened the students' technical foundation and motivated them to pursue careers and research in the semiconductor domain. Prof. Monika Mathur from SKIT and Dr. Vijay Chatterjee from CEERI lead this program.

EVENTS ORGANIZED

LECTURE SERIES ON "RECENT TRENDS IN ELECTRONICS AND COMMUNICATION ENGINEERING"

September 22-26, 2025

The Department of Electronics and Communication Engineering, Swami Keshvanand Institute of Technology, Management & Gramothan (SKIT), Jaipur, successfully organized a one-week Lecture Series on 'Recent Trends in Electronics and Communication Engineering'.

The series commenced with insightful sessions by Dr. Rajendra Mishra, who delivered expert talks on RF and Microwave Amplifier Design, followed by Dr. S. J. Nanda on Furyer and Wavelet Transforms, and Dr. A. K. Chaudhary on Signal Processing and Pipeline Architecture. The lectures covered recent advancements in RF, Microwave, and Communication Engineering, focusing on practical applications and research opportunities in these areas.

The program coordinators Dr. Vikas Pathak, Dr. Kiran Rathi and Dr. Suman Sharma, managed the series efficiently.

The sessions were designed to provide students with a deeper understanding of cutting-edge developments in electronics and communication domains.

Prof. Praveen Kumar Jain, Head of the Department, appreciated the initiative and emphasized the importance of such academic engagements in bridging the gap between classroom learning and industry-oriented research.

The lecture series concluded successfully with active participation from students, faculty members, and invited experts. The event also witnessed the presence of Mr. Chirag Agarwal, whose contribution to the program's organization was noteworthy.

The department expressed gratitude to all the speakers and participants for making the event a great academic success.



EVENTS ORGANIZED

INHOUSE TRAINING PROGRAM FOR III SEM STUDENTS

Modules Conducted:

Module 1: PCB Layout & Designing

Topics Covered: Schematic capture, PCB layering, design rules, layout techniques, and hands-on using PCB software.

RESOURCE PERSONS: Mr. Prema Ram, Mr. Dhurendra Singh, Ms. Gloria Joseph

This module focuses on the design and development of Printed Circuit Boards (PCBs). Students learn schematic design, component placement, routing techniques, and simulation using industry-standard software tools like Proteus. Hands-on sessions help them understand the entire PCB design flow.

Module 2: Basic Electronics & Components

Topics Covered: Resistors, capacitors, diodes, transistors, breadboarding, soldering basics.

Resource Persons: R. S. Ola, Dr. Kiran Rathi, Ms. Mamta Jain, Ms. Shubhi Jain

This module introduces basic electronic components such as resistors, capacitors, diodes, transistors, and their working principles. Students perform circuit-building exercises and analyze circuit behavior, which strengthens their foundational knowledge.

Module 3: Hands-on Quantum Computing

Topics Covered: Qubits, quantum gates, IBM Q experience, quantum circuits.

Resource Person: Ms. Priyanka Sharma

This advanced module provides an introduction to quantum computing concepts, including qubits, superposition, entanglement, and quantum gates. Students gain exposure to simulators and platforms like IBM Q Experience for hands-on experimentation with quantum logic.

Module 4: Introduction to Digital System Design

Topics Covered: Logic gates, combinational circuits, Karnaugh maps, basics of VHDL/Verilog.

Resource Person: Dr. Vikas Pathak

This module covers the fundamentals of digital logic circuits, number systems, logic gates, and combinational and sequential circuit design. Students design circuits using logic simulators and gain exposure to hardware description languages (HDLs).

Additional Sessions:

- Virtual Labs (NPTEL/ICT): To enhance remote lab interaction using NPTEL virtual platforms.
- Embedded Systems & R&D Trends: Industry-oriented lectures on real-time systems, IoT, and research focus.
- Communication & Soft Skills: Workshops on group discussion, interview skills, and professional communication.
- Design Thinking & Innovation: Interactive sessions on problem-solving using creativity and empathy.

Two Industrial Visits were conducted during training:

Industrial Visit 1

Venue: Akshaya Patra Food Processing Unit

Date: 25/7/2025

Industrial Visit 2

Venue: TechnoS Centre of Excellence, Jaipur

Date: 26/7/25

EVENTS ORGANIZED

INHOUSE TRAINING PROGRAM FOR III SEM STUDENTS



EVENTS ORGANIZED

EXPERT LECTURE ON "ADVANCES IN OPTICAL COMMUNICATION"

October 10, 2025

The Optica Student Chapter and IEI-ECE Student Chapter of Swami Keshvanand Institute of Technology, Management & Gramothan (SKIT), Jaipur, jointly organized an expert lecture on "Advances in Optical Communication" on Friday, 10th October 2025. The session featured Prof. Manish Tiwari, Professor, Manipal University Jaipur, as the distinguished guest and resource person.

The event commenced with a warm welcome by a faculty member from the Department of Electronics and Communication Engineering (ECE), who introduced the guest and highlighted the achievements and objectives of the Optica and IEI Student Chapters.

Prof. Tiwari delivered an insightful lecture focusing on the paradigm shift in communication technology from electronics to photonics, emphasizing that the next generation of communication (6G and beyond) will fundamentally rely on optical capacity. He elaborated on how advances in optical communication are driven by innovations in specialty optical fibers, photonic integration, and quantum effects, which form the foundation of future ultra-secure satellite and fiber communication systems.

He also discussed Space Division Multiplexing (SDM) and its role in high-capacity transmission networks, and shared his international research experience, including contributions to the BRICS QuSaF project.

Prof. Tiwari further highlighted the contributions of Indian scientists in the advancement of optical science and encouraged students to pursue research in this promising field.

The session concluded with a formal vote of thanks delivered by Prof. (Dr.) Praveen Kumar Jain, Head of the ECE Department. He appreciated the speaker's valuable insights and discussed the benefits of Optica membership, including participation in international conferences and scholarship opportunities for higher studies such as M.Tech and Ph.D.

The event was coordinated by Mr. Vineet Sharma, Ms. Lividha Choudhary, and Ms. Aaliya Praveen as student coordinators. The lecture was highly informative, interactive, and motivating, inspiring students to explore cutting-edge research in optical and photonic communication technologies.



EVENTS ORGANIZED

EXPERT ALUMNI SESSION ON "HIGHER EDUCATION OPPORTUNITIES IN ABROAD"

December 13, 2025

The Department of Electronics and Communication Engineering, Swami Keshvanand Institute of Technology, Management and Gramothan (SKIT), in collaboration with the SKIT Alumni Association, organized an informative online session on Higher Education Abroad on Saturday. The session was delivered by the institute's alumnus, Mr. Parth Bhandari, who is currently pursuing his postgraduate studies in science in Salt Lake City, USA. The online session was conducted on 13 December 2025.

At the beginning of the program, Dr. Swati Arora formally welcomed the guest speaker, followed by the felicitation of Mr. Parth Bhandari by the Head of the Department, Prof. Praveen Kumar Jain. During the session, Mr. Bhandari presented a well-structured and specially prepared presentation, providing a comprehensive overview of the entire process of pursuing postgraduate studies abroad.

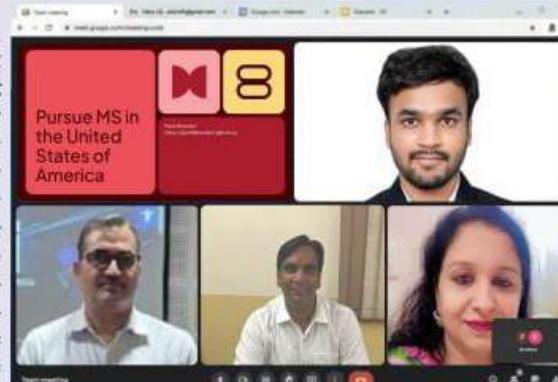
He elaborated on key aspects such as preparation strategies for entrance examinations, the required preparation timeline, application procedures, selection of universities, financial considerations, scholarship opportunities, and career prospects after completing higher education. Sharing his personal experiences, he emphasized the importance of systematic planning, consistent effort, and self-confidence in successfully securing opportunities for higher education overseas.

Towards the conclusion of the session, Dr. Rohit Mukherjee, Head of the Alumni Association, expressed his sincere gratitude to the speaker for his valuable guidance, time, and insights. The session concluded successfully, leaving the participants well-informed and motivated.

विदेश में उच्च शिक्षा विषय पर ज्ञानवर्धक ऑनलाइन सत्र

● यक्षम समाचार न्यूज

जयपुर। स्वामी केशवानंद इंस्टीट्यूट ऑफ टेक्नोलॉजी, मैनेजमेंट एंड ग्रामोथन (एसकेआईटी) के इलेक्ट्रॉनिक्स एंवं कम्युनिकेशन इंजीनियरिंग विभाग द्वारा एसकेआईटी एल्पुमनाई एसोसिएशन के सहयोग से विदेश में उच्च शिक्षा विषय पर एक ज्ञानवर्धक ऑनलाइन सत्र का आयोजन किया गया। इस कार्यक्रम के मुख्य वक्ता संस्थान के पूर्व आचार्य पार्थ भंडारी रहे, जो वर्तमान में अमेरिका के सल्ट लेक सिटी में विज्ञान में स्नातकोत्तर अध्ययन कर रहे हैं। यह सत्र 13 दिसंबर 2025 को ऑनलाइन माध्यम से आयोजित किया गया। कार्यक्रम की शुरुआत में डॉ. स्वाति अरोड़ा ने अतिथि का स्वागत किया तथा विभागाध्यक्ष प्रो. प्रवीण कुमार जैन ने पार्थ भंडारी को सम्मानित किया।



सत्र के दैर्घ्यान पार्थ भंडारी ने विशेष रूप से तैयार की गई प्रस्तुति के माध्यम से विदेश में स्नातकोत्तर अध्ययन की पूरी प्रक्रिया को विस्तारपूर्वक समझाया। उन्होंने प्रवेश परीक्षाओं की तैयारी, तैयारी के लिए आवश्यक समय, आवेदन प्रक्रिया,

विश्वविद्यालयों के चयन, आर्थिक व्यय, आत्रवृत्ति की संभावनाओं तथा अध्ययन के पश्चात करियर निर्माण जैसे महत्वपूर्ण विषयों पर विस्तृत जानकारी दी। उन्होंने अपने व्यक्तिगत अनुभव साझा करते हुए बताया कि किस प्रकार सही योजना, नियंत्रण परिप्रेक्षा और आमविद्यास के माध्यम से विदेश में उच्च शिक्षा के अवसर प्राप्त किए जा सकते हैं। कार्यक्रम के अंत में एल्पुमनाई एसोसिएशन के प्रमुख डॉ. रोहित मुख्यजी ने वक्ता को धन्यवाद ज्ञापित करते हुए उनके मार्गदर्शन, समय एवं अनुभव साझा करने के लिए आभार व्यक्त किया। इसी के साथ यह ज्ञानवर्धक सत्र सफलतापूर्वक संपन्न हुआ।



FACULTY RESEARCH ACTIVITIES

PAPERS PUBLISHED IN JOURNAL/ CONFERENCE PROCEEDINGS

TITLE: Impact of temperature on the performance of efficient GaInP single-junction solar cells with double back surface field

AUTHOR/S: Ramesh kumar, Praveen Kumar Jain

JOURNAL: MRS Advances 10, 1536–1544, September 2025

TITLE: Multi-beam formation using a parasitic patch integrated microstrip array for 5G communications

AUTHOR: Harshal Nigam, Monika Mathur

JOURNAL: UNEC Journal of Engineering and Applied Science, 5(2) (2025) 75-87, December 2025

TITLE: Millimetre-Wave RF Sensors for Non-Contact Vital Sign Monitoring in Healthcare Applications

AUTHOR/S: Shubhi Jain

JOURNAL: Carcinogenesis 10, 339–350, October 2025

TITLE: Investigation of ZnO Thin Film Characteristics Fabricated as Buffer Layer for CIGS Thin-Film Solar Cell

AUTHOR/S: Satyendra Kumar, Swati Arora

JOURNAL: International Journal of Innovative Research in Technology (IJIRT), vol. 12, no. 7, pp. 5723–5727, ISSN: 2349-6002, Dec. 2025.

TITLE: Band Gap, Thickness and Doping Consideration for CIGS Thin Film Solar Cell using SCAPS-1D

AUTHOR/S: Satyendra Kumar, Swati Arora

JOURNAL: Journal of Dynamics and Control, VOLUME 9 ISSUE 10: 37-43(2025)doi.org/10.71058/jodac.v9i10006(ScopusJournal)

TITLE: Analysis of Cu₂ZnSn as Promising Photovoltaic Absorber

AUTHOR/S: Swati Arora, Nainika Agrawal

PROCEEDING: Proceedings of 5th International Conference on "Advancements in Nano-Electronics and Communication Technologies", May 2025, ISBN: 978-81-969799-4-2.

TITLE: Study of an Efficient and Flexible Thin Film Solar Cell

AUTHOR/S: Satyendra Kumar, Swati Arora

PROCEEDING: Proceedings of 5th International Conference on "Advancements in Nano-Electronics and Communication Technologies", May 2025, ISBN: 978-81-969799-4-2.

TITLE: Optimization of IoT-based Hybrid Solar-Battery Microgrids using Modified Firefly Algorithm

AUTHOR/S: Shubhi Jain

PROCEEDING: 2nd International Conference on Electronic Circuits and Signaling Technologies (ICECST-2025) IEEE Xplore Part Number: CFP25UK9-ART; ISBN: 979-8-3315-9481-7

TITLE: Analysis of Quantum Phase Estimation Algorithm Using Quantum Fourier Transform

AUTHOR/S: Priyanka Sharma, Rukhsar Zafar

PROCEEDING: Proceedings of the International Conference on Sensors and Microsystems. ICSM 2024. Lecture Notes in Electrical Engineering, vol 1381. Springer, Singapore.

TITLE: Performance Optimization of Plasmonic Sensor Using Machine Learning and Generative Adversarial Networks

AUTHOR/S: Priyanka Sharma, Rukhsar Zafar

PROCEEDING: Advanced Computing Techniques in Engineering and Technology. ACTET 2025. Communications in Computer and Information Science, vol 2543. Springer, Cham, https://doi.org/10.1007/978-3-031-95540-2_21, August 2025.

TITLE: Design and Analysis of a Dual-Band MIMO Antenna for 5G Applications at 26.3GHz and 39.3 GHz

AUTHOR/S: Birendra Kumar Pandey, Monika Mathur

PROCEEDING: Proceedings of 5th International Conference on "Advancements in Nano-Electronics and Communication Technologies", May 2025, ISBN: 978-81-969799-4-2.



FACULTY RESEARCH ACTIVITIES

PAPERS PRESENTED IN CONFERENCES

TITLE: Design and Optimisation of a compact Microstrip patch Antenna for 24 GHz mmWave 5G wireless communications

AUTHOR/S: Rajni Idiwal, Monika Mathur

CONFERENCE: 6th International Conference on Smart Electronics and Communication (ICOSEC 2025), Kongunadu College of Engineering and Technology, Trichy, India, 24-26 September 2025.

TITLE: Performance Analysis of IGZO TFT's under Channel Length Scaling: DC, Analog and RF Perspective

AUTHOR/S: Abhinandan Jain, Praveen Kumar Jain

CONFERENCE: The 16th International IEEE Conference on Computing, Communication and Networking Technologies (ICCCNT), IIT Indore, India, 6-11 July, 2025

TITLE: Design and Performance Optimization of a Bandgap-Tailored Ga_{0.04}In_{0.96}P Single-Junction Solar Cell with a 1.36eV Bandgap for Improved Efficiency

AUTHOR/S: Ramesh Kumar, Praveen Kumar Jain

CONFERENCE: The 16th International IEEE Conference on Computing, Communication and Networking Technologies (ICCCNT), IIT Indore, India, 6-11 July, 2025

TITLE: Comparative Analysis of Machine Learning Models for Performance Evaluation of Defective Plasmonic Grating Sensor

AUTHOR/S: Priyanka Sharma, Rukhsar Zafar

CONFERENCE: 6th International Conference on Optical & Wireless Technologies 2025 (OWT-2025), National Institute of Technology (NIT) Goa, India, 18-19 December 2025.

TITLE: Improving Patient Result in Healthcare with AI-Powered Diagnostics

AUTHOR/S: Shubhi Jain, Mamta Jain

CONFERENCE: (ICEF-SSSCR 2025), ISBN-978-93-49976-79-5

TITLE: Wearable 5G Microstrip Patch Antenna Design with Bending and On-Body Performance Analysis

AUTHOR/S: Rajni Idiwal, Monika Mathur

CONFERENCE: 4th International Conference on Automation, Computing and Renewable Systems (ICACRS 2025), Mount Zion College of Engineering and Technology, Pudukkottai, Tamil Nadu, India, 10-12 December 2025.

BOOKS PUBLISHED

TITLE: Artificial Intelligence in Medical Science

AUTHOR: Dr. Shubhi Jain

PUBLISHER: Scientific International Publishing House

TITLE: Next Generation antenna Technology for 6G

AUTHOR: Dr. Shubhi Jain

PUBLISHER: Technical Press



FACULTY ACADEMIC ACTIVITIES

FACULTY DEVELOPMENTS PROGRAMS/WORKSHOPS/STTPS/SUMMER SCHOOLS/ REFRESHER COURSES ATTENDED

TITLE: AIU-AADC Sponsored Faculty Development Programme on “Next Generation of Design, Manufacturing, and Packaging Semiconductor Technologies”

DATE: 30th June to 4th July 2025.

ATTENDED BY: Dr. Pallav Rawal, Ms. Gloria Joseph

ORGANIZED BY: Gujarat Technological University, Ahmedabad, Gujarat

TITLE: Three-Day Faculty Development Programme on “Inculcating Universal Human Values in Technical Education”

DATE: 10th to 12th July 2025.

ATTENDED BY: Dr. Shubhi Jain

ORGANIZED BY: Jaipur National University, Jaipur

TITLE: Faculty Development Programme on “Prerequisites for Entrepreneur Development and Research Innovation”

DATE: 18th to 22nd August 2025

ATTENDED BY: Dr. Kiran Rathi

ORGANIZED BY: Institute of Engineering and Management, Newtown, in association with HRDC, IEM-UEM Group

TITLE: AICTE-approved Faculty Development Programme on “Quantum Communication (QT-06)”

DATE: 18th August to 11th September 2025.

ATTENDED BY: Ms. Priyanka Sharma

ORGANIZED BY: Electronics & ICT Academy, MNIT Jaipur

TITLE: Five-Day Professional Development Programme on “Recent Evolution on Microwave and Optical Technologies”

DATE: 1st to 5th September 2025.

ATTENDED BY: Dr. Shubhi Jain

ORGANIZED BY: SRM Institute of Science and Technology, Kattankulathur, Tamil Nadu, India

TITLE: International Faculty Development Programme on “Ethical Application of AI in Higher Education for Teaching, Research, and Administrative Work

DATE: 8th to 17th September 2025.

ATTENDED BY: Dr. Mukesh Arora

ORGANIZED BY: Eudoxia Research University, USA, and the Eudoxia Research Centre, India

TITLE: Faculty Development Program on “5G and Beyond Wireless Communication Challenges”

DATE: 24th November to 3rd December 2025.

ATTENDED BY: Prof. Monika Mathur

ORGANIZED BY: Electronics & ICT Academy, MNIT Jaipur

TITLE: Faculty Development Program on “Research and Publication Ethics”

DATE: 14th to 19th November 2025.

ATTENDED BY: Prof. Monika Mathur, Dr. Pallav Rawal, Dr. Shubhi Jain, Ms. Mamta Jain, Dr. Swati Arora, Dr. J. P. Vijay

ORGANIZED BY: SKIT, M&G, Jaipur

TITLE: National Workshop on “AI Integrated Smart E-Mobility towards Sustainable Development”

DATE: 18th to 22th November, 2025

ATTENDED BY: Mr. Harshal Nigam

ORGANIZED BY: SKIT, M&G, Jaipur

TITLE: AICTE Training And Learning (ATAL) Academy Faculty Development Program on Generative AI Fluency

DATE: 1st to 6th December, 2025

ATTENDED BY: Mr. Harshal Nigam

ORGANIZED BY: NASSCOM

TITLE: AICTE Training And Learning (ATAL) Academy Faculty Development Program on Next Generation Engineering Practices: Materials, Design and Manufacturing

DATE: 22nd to 27th December, 2025

ATTENDED BY: Dr. Neeraj Jain

ORGANIZED BY: Marathwada Mitra Mandal's College of Engineering, Pune



FACULTY ACADEMIC ACTIVITIES

FACULTY DEVELOPMENTS PROGRAMS/WORKSHOPS/STTPS/SUMMER SCHOOLS/ REFRESHER COURSES ATTENDED

TITLE: Five Day International Workshop on “Deep Dive into AI 2.0”

DATE: 16th to 20th December, 2025

ATTENDED BY: Dr. Kiran Rathi

ORGANIZED BY: Manipal University, Jaipur

TITLE: ATAL Faculty Development Programme on “Next-Gen VLSI and Semiconductor Systems: Integrating Machine Learning for Intelligent Design Automation”

DATE: 3rd to 8th November, 2025

ATTENDED BY: Ms. Mamta Jain

ORGANIZED BY: R.M.D. Engineering College, Tamil Nadu

TITLE: ATAL Faculty Development Programme on “Cutting Edge AI Technique for teaching and Research”

DATE: 10th to 15th November, 2025

ATTENDED BY: Ms. Mamta Jain

ORGANIZED BY: Guru Nanak Institute of Management, New Delhi

TITLE: ATAL Faculty Development Programme on “Sustainable Semiconductor Technologies: Designing Energy-Efficient Devices for Green Electronics”

DATE: 24th to 29th November, 2025

ATTENDED BY: Ms. Mamta Jain

ORGANIZED BY: Anil Neerukonda Institute of Technology & Sciences, Visakhapatnam

TITLE: AICTE Training And Learning (ATAL) Academy Faculty Development Program on Smart Energy Storage Technologies: From Research to Real-World Applications

DATE: 16th to 22th December, 2025

ATTENDED BY: Mr. Harshal Nigam

ORGANIZED BY: Government College of Engineering, Kannur

TITLE: National Workshop on “AI Integrated Smart E-Mobility towards Sustainable Development”

DATE: 18th to 22th November, 2025

ATTENDED BY: Dr. Swati Arora, Ms. Mamta Jain

ORGANIZED BY: SKIT, M&G, Jaipur

TITLE: AICTE Training And Learning (ATAL) Academy Faculty Development Program on Shaping the Future: AI in Quantum, Photonics, Nanotechnology and Intelligent Signal Processing

DATE: 1st to 6th December, 2025

ATTENDED BY: Mr. Abhinandan Jain

ORGANIZED BY: Vijaya Institute of Technology for Women, Andhra Pradesh

TITLE: AICTE Training And Learning (ATAL) Academy Faculty Development Program on Research-Based

Pedagogical Tools for Digital Transformation through Artificial Intelligence & Machine Learning in Higher Education

DATE: 8th to 13th December, 2025

ATTENDED BY: Dr. Shubhi Jain, Mr. Abhinandan Jain

ORGANIZED BY: St. Wilfred's College For Girls Jaipur

TITLE: AICTE Training And Learning (ATAL) Academy Faculty Development Program on Emerging Frontiers in Quantum Technology and Communication

DATE: 15th to 20th December, 2025

ATTENDED BY: Dr. Shubhi Jain

ORGANIZED BY: Madhav Institute of Technology, Madhya Pradesh

TITLE: AICTE approved Incorporating Universal Human Values in Education

DATE: 6th to 10th December, 2025

ATTENDED BY: Dr. Swati Arora

ORGANIZED BY: AICTE, New Delhi



FACULTY ACADEMIC ACTIVITIES

FACULTY DEVELOPMENTS PROGRAMS/WORKSHOPS/STTPS/SUMMER SCHOOLS/ REFRESHER COURSES ATTENDED

TITLE: One Week Faculty Development Program on Recent Advancements in Machine Learning: Emerging Tools, Techniques and Applications

DATE: 24th to 29th November 2025

ATTENDED BY: Dr. Swati Arora

ORGANIZED BY: SKIT, M&G, Jaipur

TITLE: Five-Day International Online Workshop on "Smart Antenna Design through CST Simulation Software"

DATE: 6th to 10th October 2025

ATTENDED BY: Dr. Shubhi Jain, Dr. Suman Sharma

ORGANIZED BY: S.A. Engineering College, Chennai

TITLE: Five Days National Level Faculty Development Programme on Recent Trends and Future Perspective of AI in Image Processing – Tools, Techniques and Applications: Research Perspective

DATE: 13th to 17th October 2025

ATTENDED BY: Dr. Rukhsar Zafar, Dr. Swati Arora

ORGANIZED BY: S.A. Engineering College, Chennai in association with Pantech Solutions Pvt. Ltd.

TITLE: NEP 2020 Orientation & Sensitization Programme (Under Malaviya Mission Teacher Training Programme – MM-TTP)

DATE: 17th September to 26th September, 2025

ATTENDED BY: Dr. Swati Arora, Ms. Gloria Joseph

ORGANIZED BY: Indian Institute of Technology Kharagpur

TITLE: Faculty Development Programme on Basics of Image Processing with Applications

DATE: 15th December to 24th December, 2025

ATTENDED BY: Ms. Gloria Joseph

ORGANIZED BY: Electronics & ICT Academy, MNIT Jaipur

TITLE: One Week National Level Faculty Development Programme on Recent Trends on AI – Text, Vision & Hardware Implementation Models

DATE: 28th August to 2nd September, 2025

ATTENDED BY: Dr. Rukhsar Zafar, Dr. Swati Arora

ORGANIZED BY: Panimalar Engineering College, in association with STEP-NITK Surathkal & Pantech Solutions Pvt. Ltd

TITLE: Faculty Development Programme on QT-07 Quantum Sensing

DATE: 26th September to 17th October, 2025

ATTENDED BY: Ms. Gloria Joseph

ORGANIZED BY: Electronics & ICT Academy, MNIT Jaipur

TITLE: Two-week Faculty Development Programme on Next Gen of Design, Manufacturing, Packaging Semiconductor Technologies

DATE: 18th August to 29th August 2025

ATTENDED BY: Dr. Rukhsar Zafar, Ms. Gloria Joseph

ORGANIZED BY: Electronics & ICT Academy, NIT Patna and Centurion University of Technology and Management, Paralakhemundi, Odisha

TITLE: Continuing Education Programme under the Summer Faculty Research Fellow Programme-2025

DATE: 14th May to 9th July 2025.

ATTENDED BY: Dr. Suman Sharma

ORGANIZED BY: Indian Institute of Technology (IIT), Delhi

FACULTY ACADEMIC ACTIVITIES

TECHNICAL/PAPER REVIEWER

- Prof. Monika Mathur served as a peer reviewer for the manuscript titled “Optimizing Engineering Photoconductive Antenna: Gap Dimensions for High-Performance Terahertz Emission and Receiver” (Manuscript Reference: MPLB-D-25-00482), for Journal Modern Physics Letters B.
- Dr. Shubhi Jain served as a Technical Reviewer at IIIrd IEEE International Conference on Networks, Multimedia and information Technology (NMITCON) Organized at Nitte, Meenakshi Institute of Technology, Bengaluru India, 1st - 2nd August, 2025 in association with IEEE Bangalore Section.
- Ms. Rajni Idiwal served as a Technical Reviewer at the 2025 Second IEEE International Conference for Women in Computing (INCOWOCO 2025), organized by GHRCEM Pune and technically co-sponsored by IEEE Pune Section and IEEE WIE, 14–15 November 2025
- Mr. Harshal Nigam reviewed the paper titled "Design, modeling and experimental characterization of a patch antenna for SWIR/MWIR detection", Manuscript reference: ERX-114314 for Engineering Research Express (Scopus Indexed Q2 Journal)
- Dr. Neeraj Jain served as a reviewer(Member TPC) in 11th IEEE International Symposium on Smart Electronic Systems, December 13-17, 2025 held at MNIT, Jaipur.

SESSION CHAIRED

- Prof. (Dr.) Praveen Kumar Jain chaired a technical session on the topic entitled “Future Roadmap: AI driven Digital Transformation in Academia” during AICTE-Sponsored Faculty Development Programme (FDP) on the theme: “Research-Based Pedagogical Tools for Digital Transformation through AI and ML in Higher Education” held at St. Wilfred College for Girls, Jaipur.
- Dr. Vikas Pathak chaired a technical session on the topic entitled “Emerging Technology” during the 6th International Conference on Data Science and Applications held at MNIT Jaipur.
- Prof. Monika Mathur and Dr. Rukhsar Zafar chaired a technical session during the Sixth International Conference on Optical & Wireless Technologies 2025 (OWT 2025) held at NIT Goa, India

AWARD /RECOGNITION

- Ms. Priyanka Sharma and Dr. Rukhsar Zafar received **Best Paper Award** for the paper titled “Comparative Analysis of Machine Learning Models for Performance Evaluation of Defective Plasmonic Grating Sensor” during 6th International Conference on Optical & Wireless Technologies 2025 (OWT-2025), National Institute of Technology (NIT) Goa, India, 18–19 December 2025.
- Mr. Harshal Nigam (Electromagnetic Theory) received the recognition of Topper of this course in NPTEL-SWAYAM.
- Mr. Abhinandan Jain (Digital Circuits) received mentor certificate from NPTEL-SWAYAM.
- Ms. Priyanka Sharma successfully qualified in Qniverse Developer Certification conducted by CDAC Bengaluru.

STUDENT ACHIEVEMENTS

PLACEMENT IN MICRON TECHNOLOGY PLACEMENT IN JYOTI ELECTRONICS

Micron Technology is a globally renowned semiconductor company specializing in memory and storage solutions, including DRAM, NAND flash, and SSD technologies. The company plays a critical role in advanced electronics, data centers, artificial intelligence, automotive electronics, and next-generation communication systems. Selection in such a core ECE multinational organization highlights the students' strong technical competence and industry readiness. The technical guidance and research exposure provided by CSIR-CEERI, Pilani, played a pivotal role in preparing the students for this achievement.

Four students of the 2025 graduating batch have secured off-campus placements at Micron Technology.

- **Ms. Nandini Khandelwal**
- **Ms. Navneet Kaur**
- **Mr. Chirag Gurnani**
- **Mr. Abhijeet Giri**

These achievements collectively demonstrate the institution's commitment to industry-aligned education, core domain skill development, and research-driven learning, and stand as a testament to the consistent efforts of students, faculty, and supporting research organizations.

Jyoti Electronics is a reputed Indian company engaged in electronics manufacturing, industrial electronics, embedded systems, and power electronics solutions, catering to sectors such as automation, instrumentation, and control systems. Selection in Jyoti Electronics reflects the students' practical skills, hands-on laboratory training, and strong understanding of core engineering concepts.

Following ECE students :

- **Mr. Ayush Arora**
- **Mr. Devendra Kumar**
- **Mr. Irfaan Khan**

and one ME student:

have been successfully selected by Jyoti Electronics as a part of its collaboration with SKIT.

NPTEL SWAYAM Certifications

List of Students (Jul-Dec,2025)

S.NO	Name of the Student	Name of the course	Result(%)	Remark (Meritorious mentions)
1.	Aaliya Parveen	The Joy of Computing using Python	84	Elite+Silver
2.	Ashita Bansal	English Language for Competitive Exams	78	Elite+Silver
3.	Rajveer singh Naruka	Developing Soft Skills and Personality	77	Elite+Silver
4.	Diya Arora	Public Speaking	69	Elite
5.	Ashita Bansal	Soft Skill Development	69	Elite
6.	Khwahish Rogha	Digital Circuits	67	Elite
7.	Khwahish Rogha	Hardware Modeling using Verilog	66	Elite
8.	Aditya Kumar Singh	VLSI Design Flow: RTL to GDS	66	Elite
9.	Khushi Jangir	The Joy of Computing using Python	65	Elite
10.	Aditya Kumar Singh	Introduction to Large Language Models (LLMs)	62	Elite
11.	Prathmesh Narwaria	Digital Circuits	57	Successfully completed
12.	Rahul Beniwal	Advance Course in Social Psychology	56	Successfully completed
13.	Rahul Beniwal	Digital Circuits	55	Successfully completed
14.	Khushi Jangir	Digital Circuits	54	Successfully completed
15.	Hitarth Sharma	Introduction to Machine Learning	54	Successfully completed

NPTEL SWAYAM Certifications

S.NO	Name of the Student	Name of the course	Result	Remark (Meritorius mentions)
16.	Aaliya Parveen	Digital Circuits	53	Successfully completed
17.	Anshul garg	Introduction to Semiconductor Devices	52	Successfully completed
18.	Ashish Gurjar	Digital Circuits	52	Successfully completed
19.	Vivek Prajapat	A Basic Course on Electric and Magnetic Circuits	52	Successfully completed
20.	Vineet Sharma	Digital Circuits	52	Successfully completed
21.	Komal	Digital Circuits	50	Successfully completed
22.	Sahil bohra	Intelligent Feedback and Control	49	Successfully completed
23.	Rajveer singh Naruka	Digital Circuits	48	Successfully completed
24.	Sandeep Kumar	Semiconductor Devices and Circuits	47	Successfully completed
25.	Keshav saini	Problem Solving through Programming in C	45	Successfully completed

List of Faculties (Jul-Dec,2025)

S.NO	Name of the Faculty	Name of the course	Result	Remark (Meritorius mentions)
1.	Harshal Nigam	Electromagnetic Theory	72	Topper
2.	Suman Sharma	Mental Health and Wellbeing	92	Gold
3.	Gloria Joseph	Computer Architecture	54	Successfully completed



“SAFAR”



किसी भी सफ़र की सबसे खूबसूरत बात,
मिलकर चलें हम, सबका हो साथ...



जब सबका हो साथ, राहें हो जाती आसान,
पर ना हो ऐसा, तो ना करो मन छोटा...

व्यों कि यही मुश्किल सफ़र,
कराता है एहसास...

कि हम हैं कितने मज़बूत और खास
जिन्हें खुद ही बनाते हैं रास्ते,
और देना है खुद का साथ...



यूँ ही कट जाएगा ज़िंदगी का ये सफ़र,
हर मोड़ पर मिलता नया कोई सबक-ए-सफ़र...
बस चलते रहना है, मेरे मन का ये कहना है
बस चलते रहना है....



Kiran

ALUMNI CORNER

Dreams don't come true overnight; they are built step by step, with hard work, discipline, and consistency.

Studying well is not just about scoring marks. It is about building knowledge, sharpening your mind, and preparing yourself for the challenges that life will put before you.

Every hour you dedicate to your books, every concept you understand, every mistake you correct—these are not small things, they are the building blocks of your success.



There will be days when you feel tired, days when distractions seem stronger than your willpower. But remember this—success never comes easy. The people who achieve their dreams are not the ones who never faced struggles; they are the ones who refused to give up despite them.

Always keep your goal in front of your eyes. Believe in yourself. Respect your time. Value your teachers and parents who are guiding you.

And most importantly—never stop dreaming big, because dreams backed with hard work turn into reality. Your future is not written yet—it is waiting for you to write it.

So study with dedication, walk with confidence, and one day, you will not only achieve your dreams but also inspire others to achieve theirs.

Best Wishes!



**MOHIT YADAV, 2024 BATCH
INDIAN COAST GUARD,
AIR 4, INDIAN ARMY**



Always be happy about the things that you are doing. Believe and be confident in your dreams. Surround yourself with people who are always there to support you. Stop comparing yourself with others and achieve what you really want to achieve.

**JAGRATI MEENA
2024 BATCH
HPCL**

ALUMNI CORNER

Dear Juniors,

When you stand at the crossroads of engineering and career choices, it's natural to feel uncertain. Some of you may already have a clear goal, while others are still figuring it out—and that's perfectly okay. What matters most is that you keep moving forward with discipline and confidence, because every bit of effort you invest today will open doors tomorrow.

The world outside the campus will challenge you, but if you prepare yourself with patience and consistency, no challenge will be too big.



Remember, success is not just about marks or a job title. It's about the mindset you build, the skills you sharpen, and the ability to stay steady when things get tough.

At the same time, don't forget that these years won't come back. Make memories, laugh hard, travel with your friends, and celebrate the small wins. Enjoying the journey doesn't mean losing focus—it means refueling yourself to go even further.

Today, wearing the Armed Forces uniform fills me with pride. But more than the destination, I value the path that brought me here—the lessons learned, the people I met, and the balance I maintained along the way. I hope each of you finds not just a career, but also joy in the process of building it.

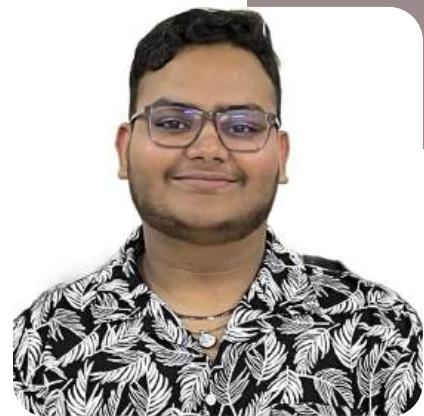


**JAI PRAKASH ANAND,
2024 BATCH
INDIAN MILITARY ACADEMY,
DEHRADUN**

ALUMNI CORNER

From ECE to E-Commerce: My Professional Journey as a Software Engineer

My journey from Electronics and Communication Engineering at SKIT to becoming a Software Engineer at Sarvika Technologies has been anything but linear. While studying ECE, I always dreamed of working in IT, which motivated me to be proactive, learn quickly, and make bold decisions.



My first big step came when I started working as a Web Analyst at Sarvika Technologies during my final months of college. Within five or six months of focused effort, I earned a promotion to Software Engineer in the JavaScript Team—a role I had aspired to and now take great pride in.

Working at Sarvika has exposed me to sophisticated e-commerce solutions for American clients, where I enjoy tackling complex and innovative challenges that have a direct impact on user experience.

Looking back, SKIT gave me invaluable exposure to diverse fields and helped me prepare for challenges beyond my core discipline. One of my proudest moments was winning 2nd place in the Science Day Project Exhibition held by the Government of Rajasthan in February 2024—a milestone that reminded me I could excel by embracing innovation.

If I could offer one piece of advice to current SKIT students, it would be this: focus on building your skill set rather than dwelling on just one goal. College is the perfect place to try new things and broaden your abilities. Adaptability and a strong skill set will put you in a position to succeed, whatever the field or technology.

I'm always happy to connect and help others shape their own journeys—feel free to reach out to me on LinkedIn if you want to talk or need advice!

VANSHAJ KATARIA
SOFTWARE ENGINEER
SARVIKA TECHNOLOGIES, JAIPUR

STUDENT CORNER

Learning ECE: A Student's Perspective

Electronics and Communication Engineering (ECE) is often seen as a challenging branch because it involves mathematics, circuits, and abstract concepts. However, from a student's point of view, ECE is more about developing a logical and systematic way of thinking than just studying theory.



Subjects like Signals and Systems help us understand how real-world information such as sound and data can be represented using graphs and equations. While drawing signals may seem simple, it improves our ability to visualize problems and understand how systems behave with time and frequency.

Analog Electronics introduces the practical side of engineering. Concepts such as biasing, Q-point, amplifiers, and feedback show that real circuits are not ideal. Stability, reliability, and proper design are often more important than achieving very high gain. This subject helps students relate classroom learning to real electronic systems.

ECE also encourages system-level thinking. Instead of focusing on individual components, we learn how different blocks work together to form a complete system. This approach is useful not only in electronics but also in modern technologies like communication networks, IoT, and automation.

Laboratory experiments and online learning platforms such as NPTEL support classroom teaching by strengthening concepts through practical understanding. They play an important role in building confidence in technical subjects.

In conclusion, ECE is not just about circuits and equations. It helps students develop analytical thinking, problem-solving skills, and a strong technical foundation. These qualities prepare ECE students to adapt and grow in a rapidly changing technological world.

**VINEET SHARMA
3RD SEMESTER
ECE**

STUDENT CORNER

Inside the Chip: A World of Networks

Think of a bustling city with delivery trucks, roads, and traffic lights. Imagine scaling a city like that down to the size of a small silicon chip inside a laptop or a mobile phone. This is exactly the scenario in today's modern chip.



A silicon chip is not only an arrangement of transistors but is, in fact, a small city where memory chunks, processor cores, and other specialized components, including an AI accelerator, need to communicate with one another.

In the past, chips relied on one "main road" called a bus to transfer information. This worked well when they had only a few components. As chips got more and more powerful with many cores and large chunks of memory, this main road got congested. Information transferred at slower speeds, and inefficiencies increased as if they lived right in the middle of rush-hour traffic.

To solve this issue, engineers came up with the NoC, or Network-on-Chip. Just imagine a city road network with multiple routes, crossings, and traffic signals. In such a setup, "data" is converted into "packets" like delivery vehicles that carry "messages." "Tiny traffic controllers" with the name "routers" manage these "packets" meant for the "faster route." Even if there are "roads," multiple "data" transmit simultaneously with no jam on the chip, thus it is fast, reliable, and power-efficient.

In this invisible city, all things are a decision. How the "roads" are laid out, when the "signals" go off, and how the information moves in the "network" have all been carefully engineered. It is the invisible network at work in the world around us that allows your games to begin when and how you want them to, your videos to stream without buffering, and the AI processes to happen in the AI features.

In other words, every chip hides a well-planned network city inside it. To understand this world is to show that modern technology is not only powerful parts but how well they converse.

HARSHITA KHATKAR
8TH SEMESTER
ECE

'ऑप्टिकल कम्युनिकेशन के नवीन आयाम' विषय पर व्याख्यान



● यक्षम समाचार न्यूज़

जयपुर। स्वामी केशवानंद इंस्टीट्यूट ऑफ टेक्नोलॉजी, मैनेजमेंट ए ग्रामोथान (एसकेआईटी), जयपुर में आईआइ स्टूडेंट चैप्टर अं ऑप्टिका स्टूडेंट चैप्टर के संयुक्त तत्वावधान में ऑप्टिकल कम्युनिकेशन नवीन आयाम विषय पर विशेषज्ञ व्याख्यान एवं संवाद सत्र का सफ आयोजन किया गया। इस सत्र के मुख्य वक्ता प्रोफेसर मनीष तिवार मणिपाल यूनिवर्सिटी जयपुर के प्रोफेसर, ऑप्टिका एम्बेसर (लाइफटाइम) एवं आईटीई राजस्थान चैप्टर के चेयरमैन रहे। कार्यक्र का शुभारंभ प्रो. प्रवीन कुमार जैन, हेड ईसीई, द्वारा अतिथि प्रो. मनी तिवारी का स्वागत एवं अधिनंदन के साथ हुआ। उन्होंने अतिथि वक्ता व परिचय और उनके योगदानों का उल्लेख किया।

एसकेआईटी के ईसीई विद्यार्थियों की इंडस्ट्रियल विजिट हुई सम्पन्न



जयपुर। (आस-पास ब्लूरो) स्वामी केशवानंद इंस्टीट्यूट ऑफ टेक्नोलॉजी, मैनेजमेंट ए ग्रामोथान (एसकेआईटी), जयपुर के इलेक्ट्रॉनिक्स एवं कम्युनिकेशन इंजीनियरिंग विभाग के विद्यार्थियों ने उद्योग जगत की व्यवहारिक जानकारी के उद्देश्य से बीएसएनएल, जयपुर एवं टेक्नोस इंस्ट्रूमेंट्स, सितापुरा का इंडस्ट्रियल विजिट सम्पन्न किया। विद्यार्थियों ने बीएसएनएल कार्यालय में टेलीकॉम इंडस्ट्री की कार्यप्रणाली, नेटवर्क ऑपरेशन और आधुनिक संचार तकनीकों के बारे में गहन जानकारी

श्रीगंगानगर, मंगलवार, 07 अक्टूबर 2025

सीएसआईआर, जयपुर एक माह का 'एडवांस्ड आईओटी ट्रेनिंग प्रोग्राम' आयोजित कर रहा है एसकेआईटी के विद्यार्थी प्रशिक्षण के लिए चयनित

सीमा सन्देश # जयपुर।

सीएसआईआर-केन्द्रीय इलेक्ट्रॉनिक्स इंजीनियरिंग अनुसंधान संस्थान (सीरी), जयपुर द्वारा 6 अक्टूबर से 31 अक्टूबर 2025 तक विभिन्न शैक्षणिक संस्थानों के सहयोग से एक माह का 'एडवांस्ड आईओटी ट्रेनिंग प्रोग्राम' आयोजित किया जा रहा है। इस



के इलेक्ट्रॉनिक्स और कम्युनिकेशन इंजीनियरिंग विभाग के 8 प्रतिभाशाली छात्रों का चयन इस

विकास और नवाचार आधारित परियोजनाओं में मार्गदर्शन प्रदान करेंगे। चयनित छात्रों के लिए यह प्रशिक्षण उनके करियर विकास, तकनीकी दक्षता और नवाचार कौशल को नई ऊँचाइयों तक पहुँचाने में महत्वपूर्ण भूमिका निभाएगा। एसकेआईटी जयपुर के छात्रों की यह उपलब्धि संस्थान की तकनीकी उत्कृष्टता, शिक्षण गुणवत्ता और

एसकेआईटी और ज्योति इलेक्ट्रॉनिक्स के बीच हुआ एमओयू

कामयाब कलम, जयपुर

स्वामी केशवानंद इंस्टीट्यूट ऑफ टेक्नोलॉजी, मैनेजमेंट ए ग्रामोथान, जयपुर के सेंटर ऑफ एक्सिलेंस इन एंटीना, माइक्रोवेव एंड

आरएफ इंजीनियरिंग ने अहमदाबाद स्थित ज्योति इलेक्ट्रॉनिक्स के साथ एम ओ यू किया। एसकेआईटी की ओर से निदेशक जयपाल मील ने एमओयू पर हस्ताक्षर किए। यह सहयोग उद्योगशैक्षणिक समन्वय को नई दिशा देगा। एमओयू का ऑप्टिकल आदानप्रदान अहमदाबाद में आयोजित



एस.ए.सी. टेक्नोलॉजी डे कार्यक्रम के दौरान हुआ। इस अवसर पर एसएसईसरों के कई विशेषज्ञ मौजूद रहे और अंतरिक्ष अनुसंधान तथा आगामी तकनीकों में नवाचारों पर चर्चा की। दो समन शर्मा एमोमिंग

रिसेंट ट्रेंस इन इलेक्ट्रॉनिक्स एंड कम्युनिकेशन इंजीनियरिंग पर व्याख्यान शुरू



● दिव्य जनजागृति समाचार

जयपुर। स्वामी केशवानंद इंस्टीट्यूट ऑफ टेक्नोलॉजी के इलेक्ट्रॉनिक्स एवं कम्युनिकेशन इंजीनियरिंग विभाग द्वारा बन वीक



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