



**National Workshop on
PCB Design and Fabrication (PDF-2K19)
19th-21st September, 2019
Sponsored by: IEEE MTT SKIT Student Chapter**

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ABOUT SKIT

The institute is inspired from the learning's of Swami Keshvanand Ji. The Institute is affiliated to Rajasthan Technical University, Kota and approved by All India Council for Technical Education (AICTE), Ministry of Human Resource Development, and government of India to offer Doctoral, Postgraduate and Graduate Courses in Engineering. The B. Tech. Programme of CS, EE, ME, ECE and IT departments of institute are accredited by National Board of Accreditation (NBA).

DEPARTMENT OF ECE

We have a rich tradition in research and teaching. All efforts are subtly harnessed with the aim of preparing the budding engineers to face the challenging dimensions of technical excellence in various areas of engineering.

Printed Circuit Boards provide electronic connections and mechanical support to electronic components and assemblies. Conductive tracks, pads and other features etched from copper sheets laminated onto a non-conductive substrate to connect capacitors, resistors or active devices which are generally soldered on the Printed Circuit Board. We can't rely upon bread board for substantially increased number of components and for smaller packaging sizes of Integrated circuits that's makes PCB more essential. It is more of an art than just science with lots of scope for designer.

Programme topics

- A Computerized capture of electronic designs**
- B Introduction to PCB Layout**
- C PCB Designing (Single and Multi-Layer)**
- D Testing Issue and Test Procedure**

Objective of the workshop

- **Understanding of the steps and processes involved in PCB design Process**
- **PCB Fabrication Process**
- **Schematic Capture using Industry Standard Schematic Capture tools**
- **Preparation of Board for Layout**
- **Gerber file Creation and View**
- **Practical hands on to demonstrate PCB fabrication of a circuit.**

Speakers

- **Mr. U. Nagaraj, GM (Quality and Technical), Micropack Ltd., Bengaluru.**
- **Ms. Meenakshi Jwala, Director, Electronic Testing & Development Corporation, Jaipur.**
- **Dr. S. K Bhatnagar, Director (Research), SKIT Jaipur.**

Programme coordinators

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Registration

Registration is open to students, faculty, industry persons, research scholar of **all the branches of Engineering**. Participants will be admitted on a *first-come first-serve basis*. Selected participants will be notified on or before 16th September, 2019.

Fee

- (A) The registration fees for Participant (outside SKIT) is Rs.600/-
- (B) The fee covers the participation in the programme along with Tea & lunch on all the days of the workshop.
- (C) The travel and other expenses would have to be borne by the participants or their parent organizations.
- (D) Lodging for very limited number of outside participants is available on first-come first-served and additional payment basis.
- (E) The registration is to be done by filling the Google form at link: <https://forms.gle/UDg8U3aeYNhVvJHu7>

❖ **No Registration Fees for IEEE and IETE Members**

Further query: Please contact the programme coordinators.

Organised By:

**Department of Electronics and Communication Engineering
Swami Keshvanand Institute of Technology, Management and Gramothan, Ramnagar, Jagatpura, Jaipur**